

Fig. 1

24 \	26
Thread 1	Thread 2
mov r1 = r2	mov
1d r1 = [r2] ~ 30	- mov
br < 1 (conditional) — 28	mov
addr3 = r5 + r6	1d 32
sub r5 = r2 + r5	1d
11 mov	st

Fig. 2

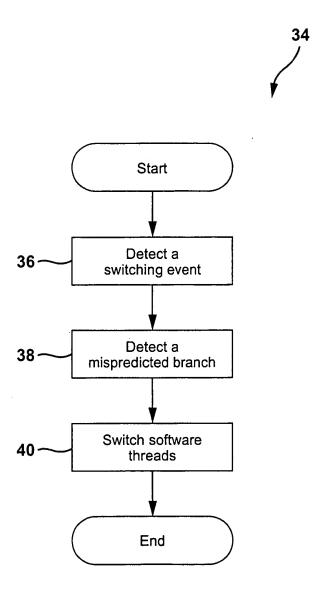


Fig. 3

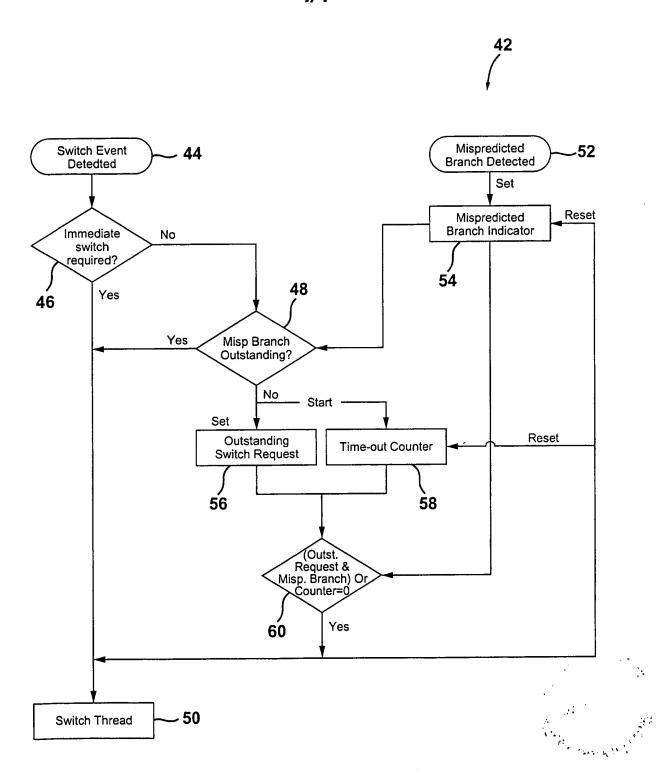


Fig. 4